

1/9

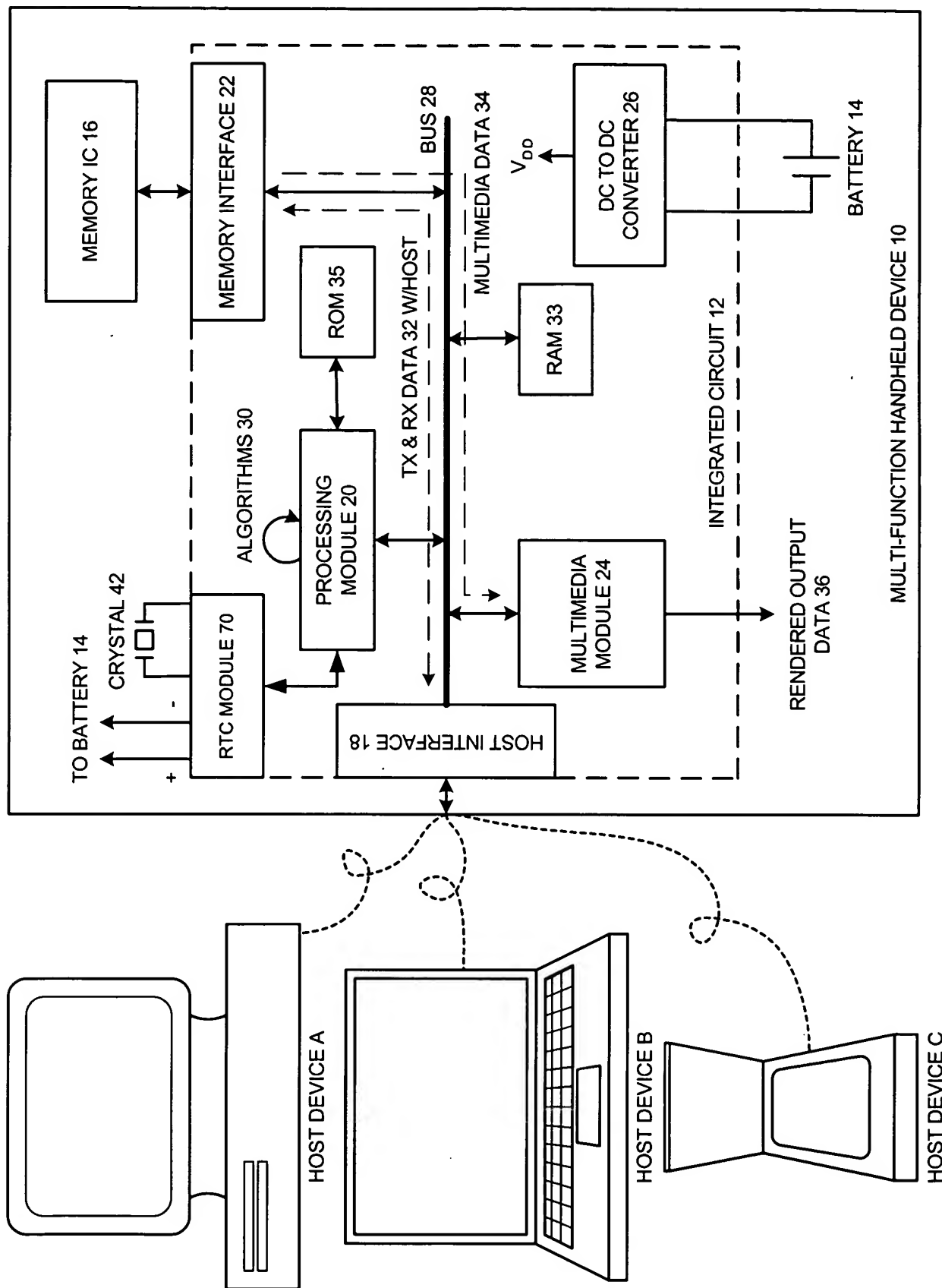


FIG. 1

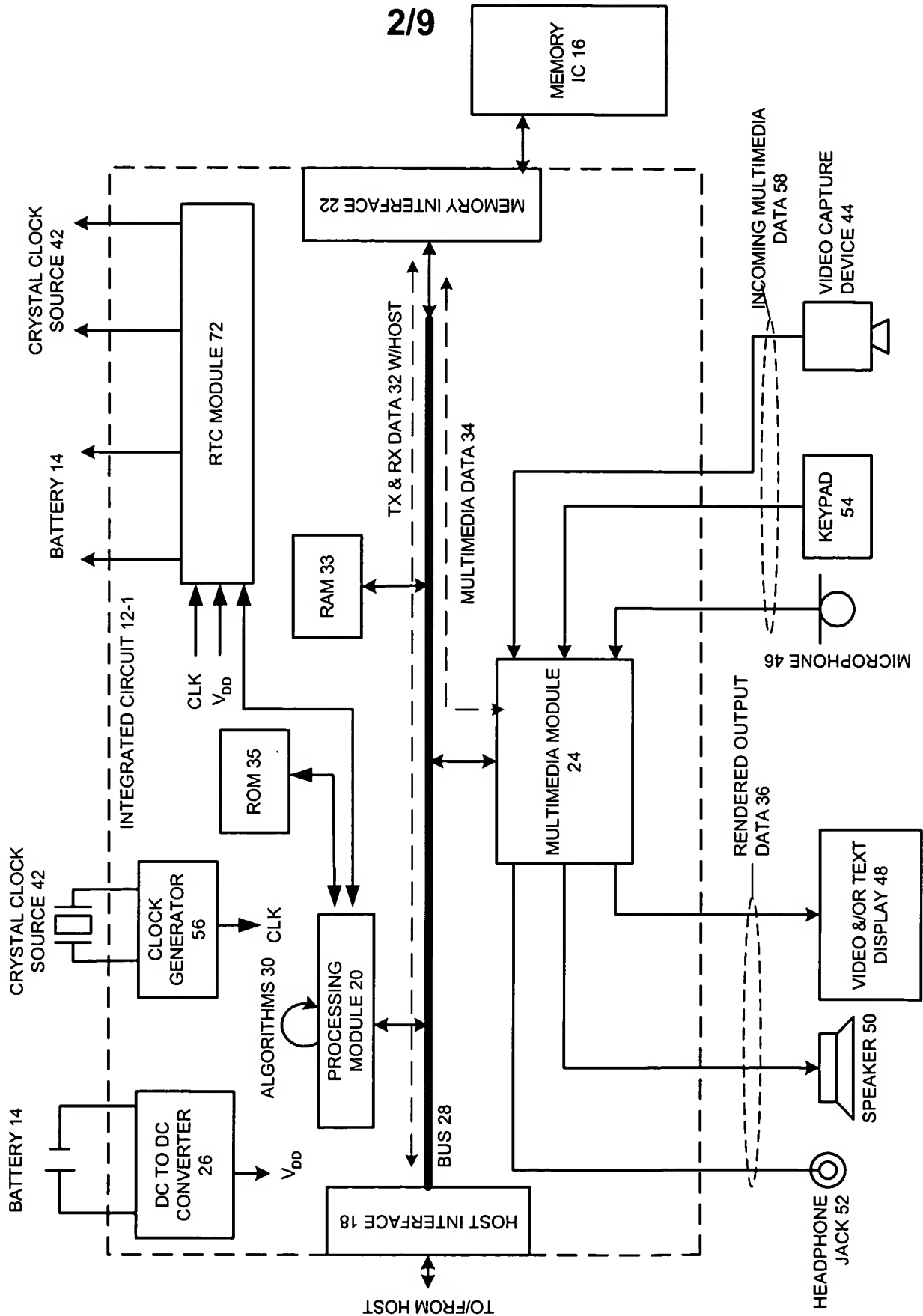
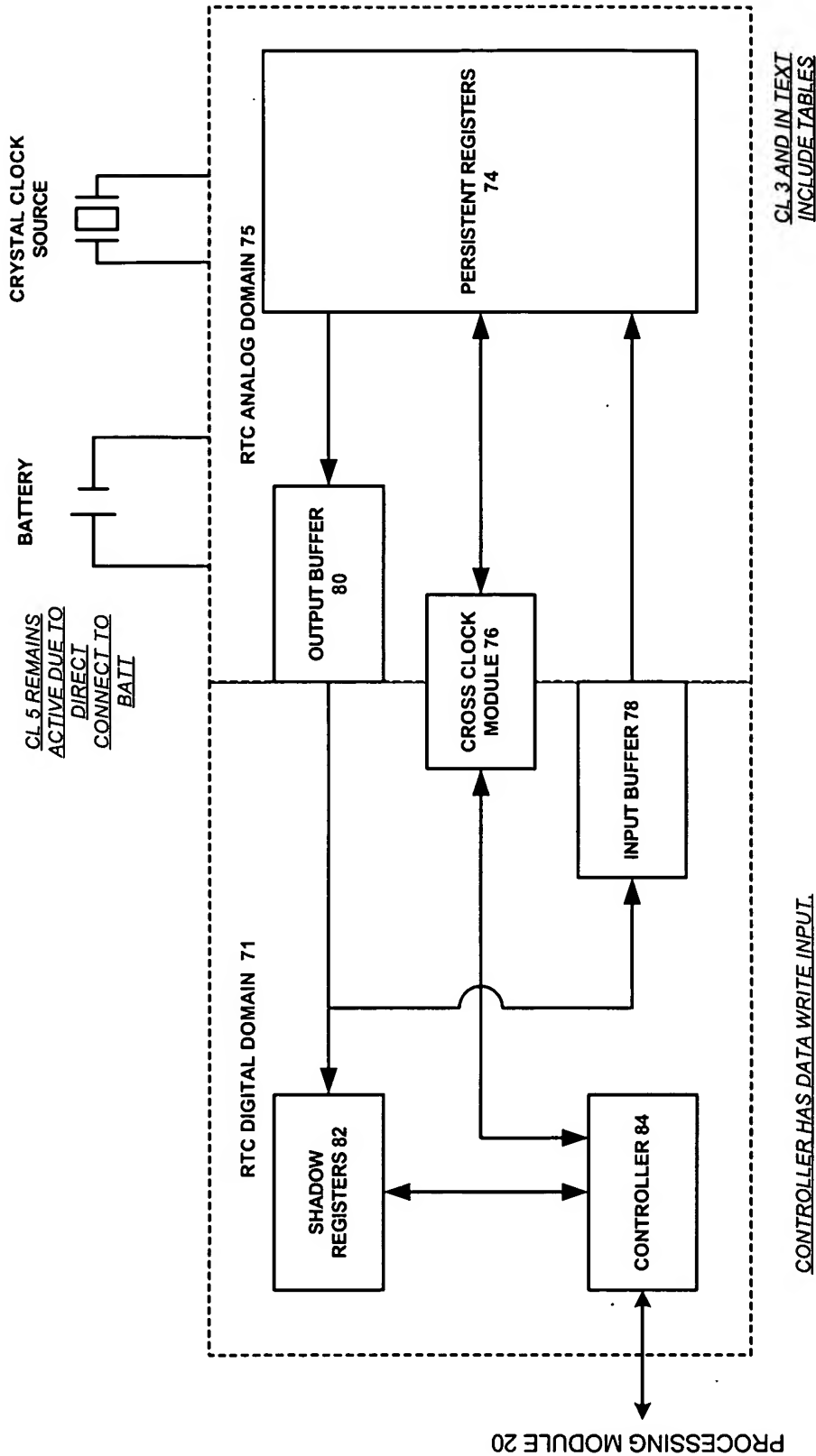


FIG. 2

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CL 3 AND IN TEXT
 INCLUDE TABLES

FIG 3
real time clock module 72

CONTROLLER HAS DATA WRITE INPUT.
 DATA READ OUTPUT. READ REQUEST.
 WRITE REQUEST. AND RW ACK

DISCUSS W/ REFERENCE TO FIGURE.
 CLAIMS 8 - 13. AS SEPARATE
 EMBODIMENTS OF DATA EXCHANGE

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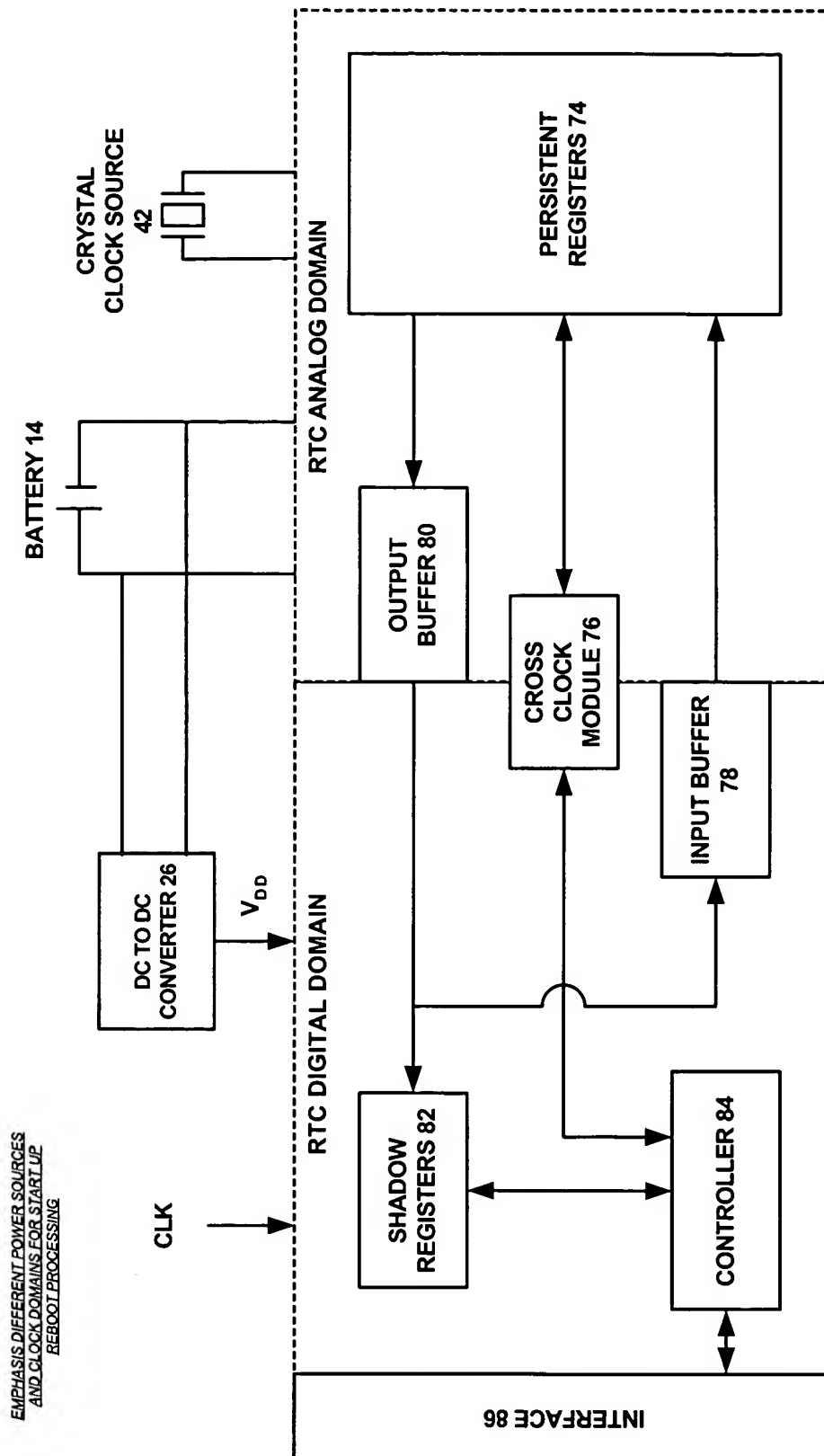


FIG 4
 real time clock module 72

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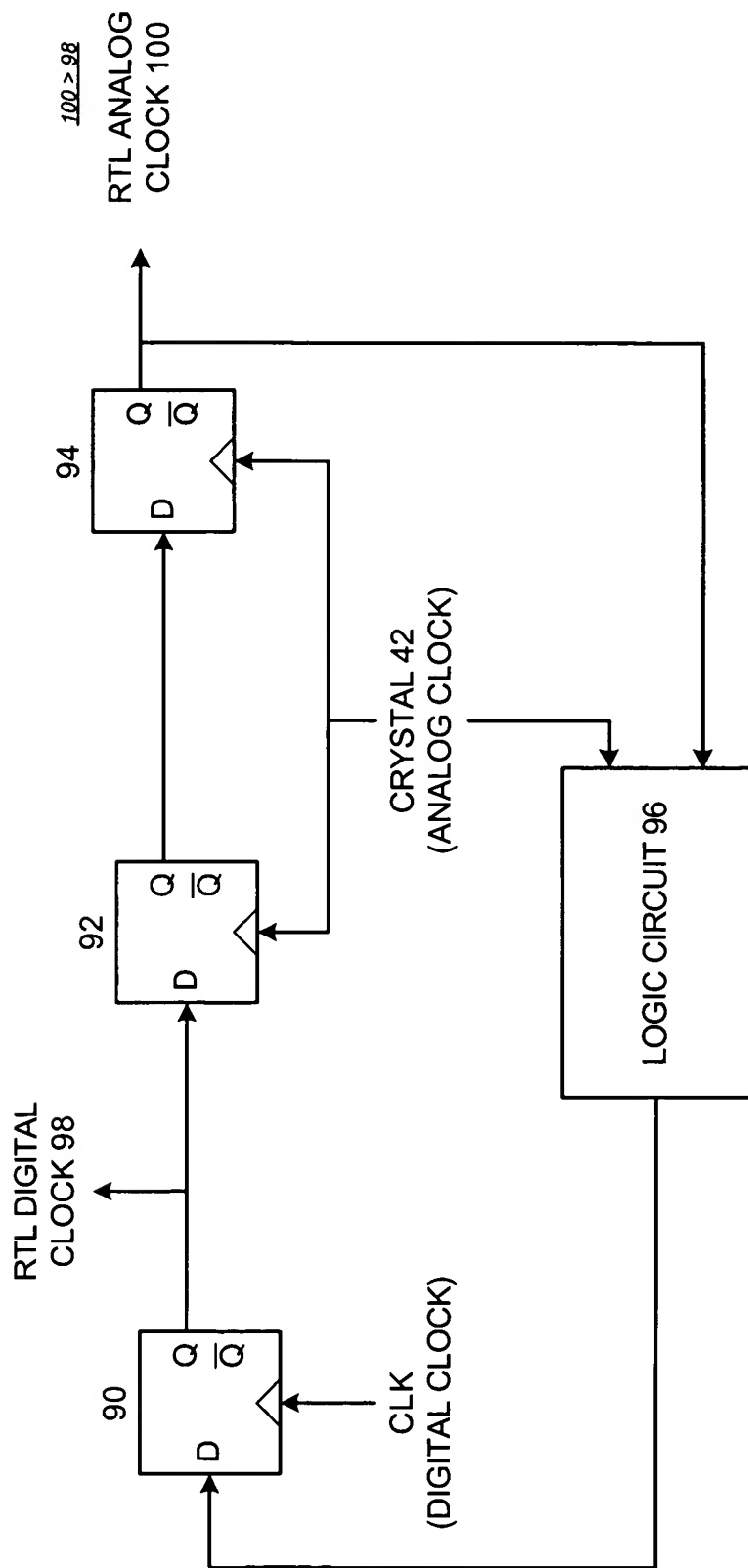
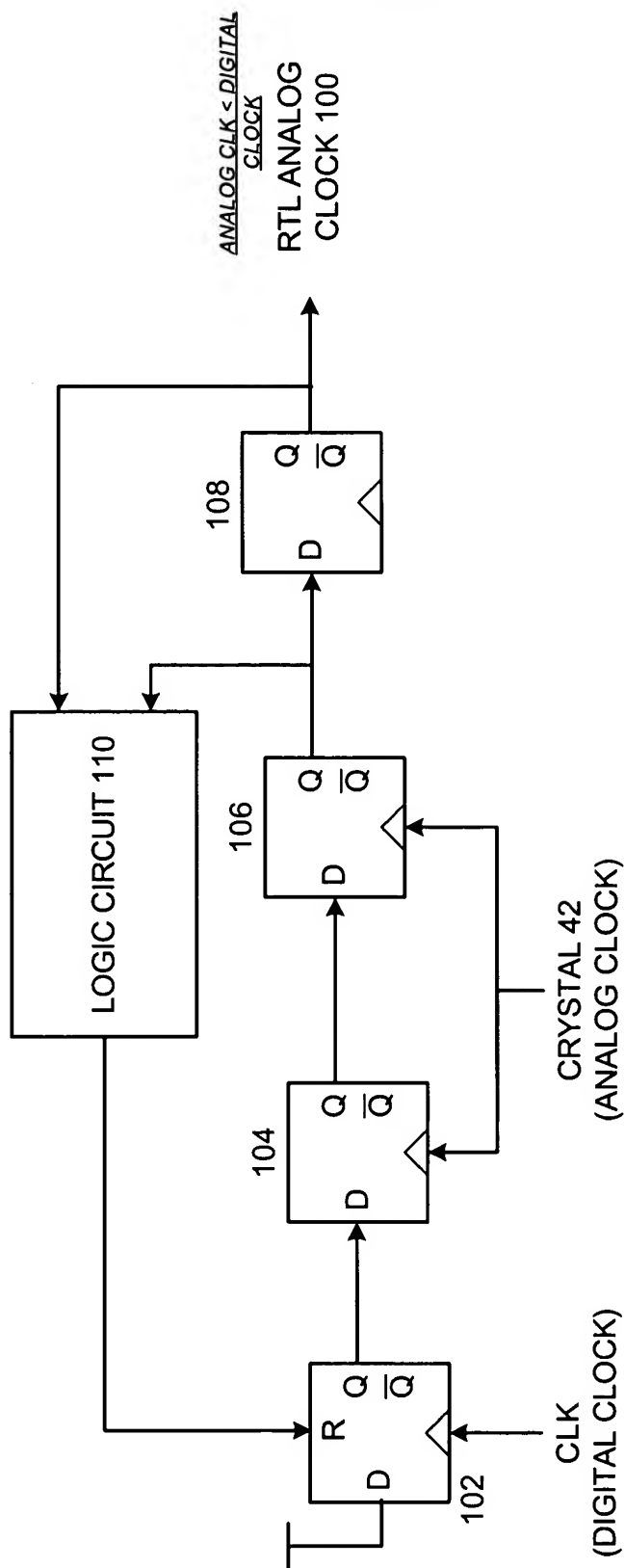


FIG. 5
CROSS CLOCK MODULE 76

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SYNCHRONIZATION BETWEEN CLOCK
 DOMAINS IS THE PRIMARY FUNCTION

FIG. 6
CROSS CLOCK MODULE 76

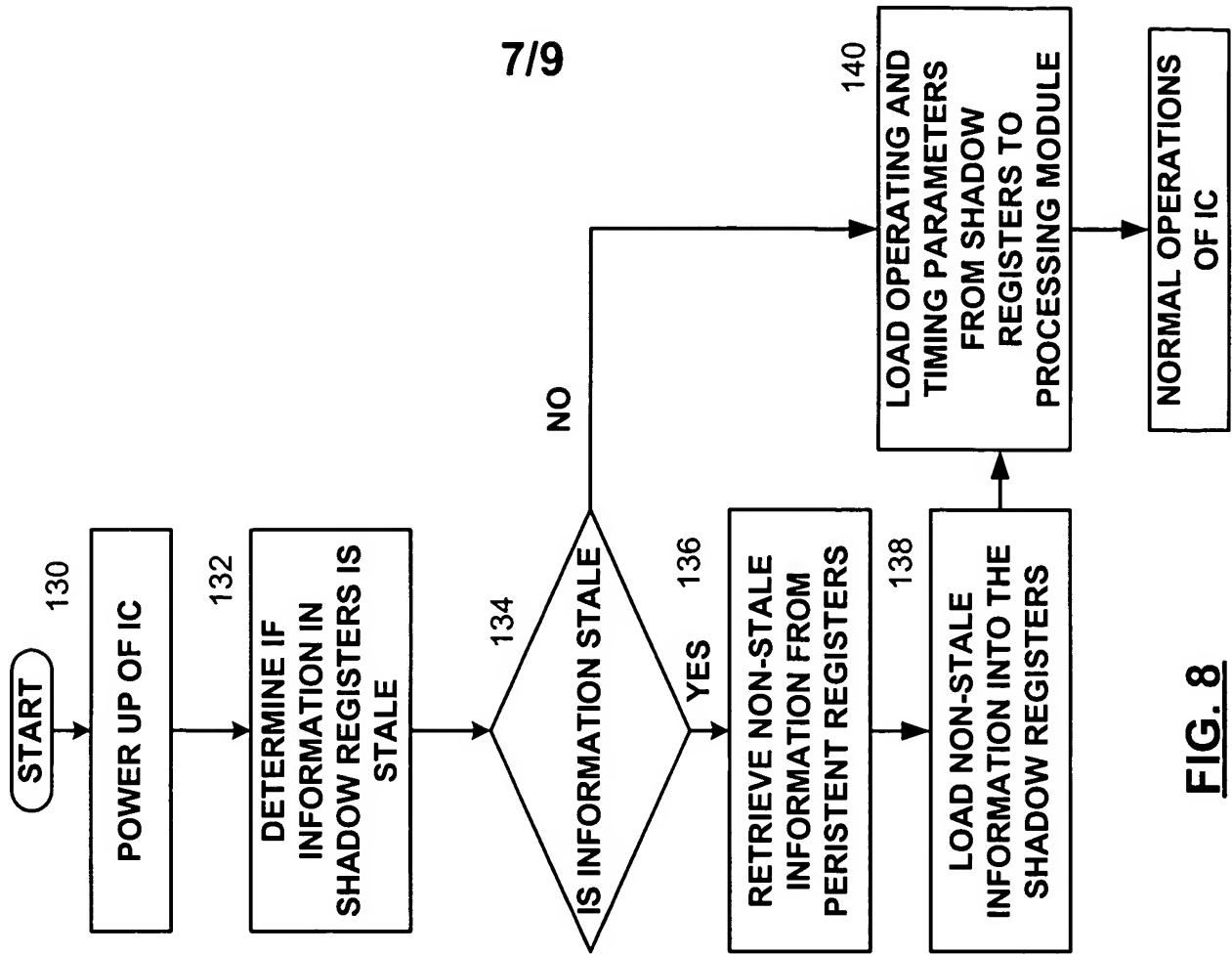


FIG. 8

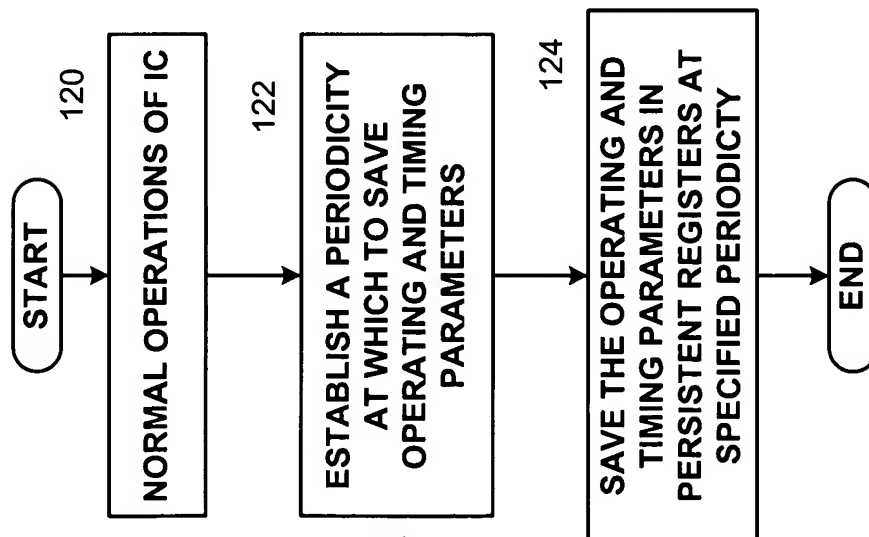


FIG. 7

REFERENCE THE
 TABLE THAT LISTS
 THESE
 PARAMETERS

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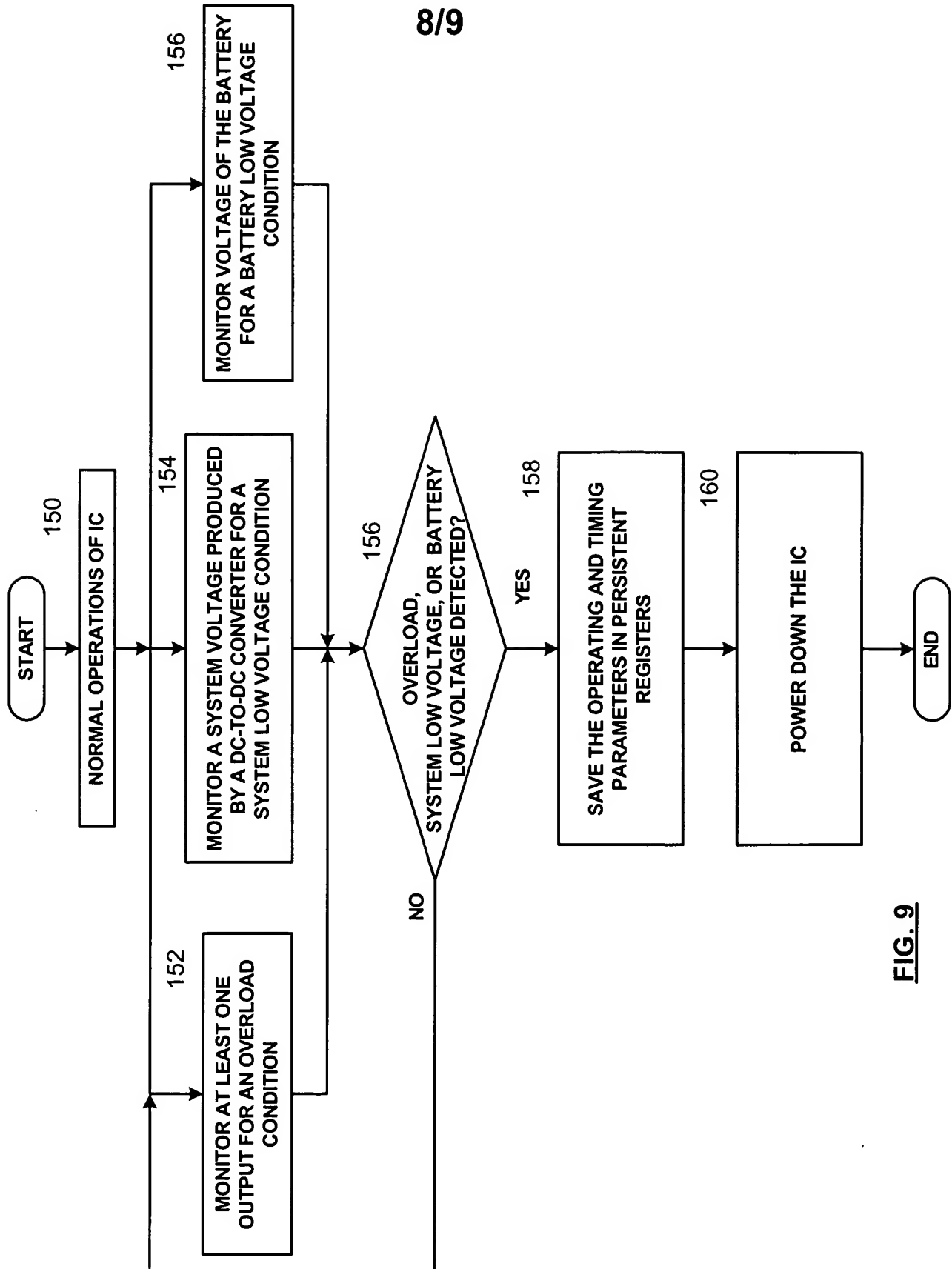


FIG. 9

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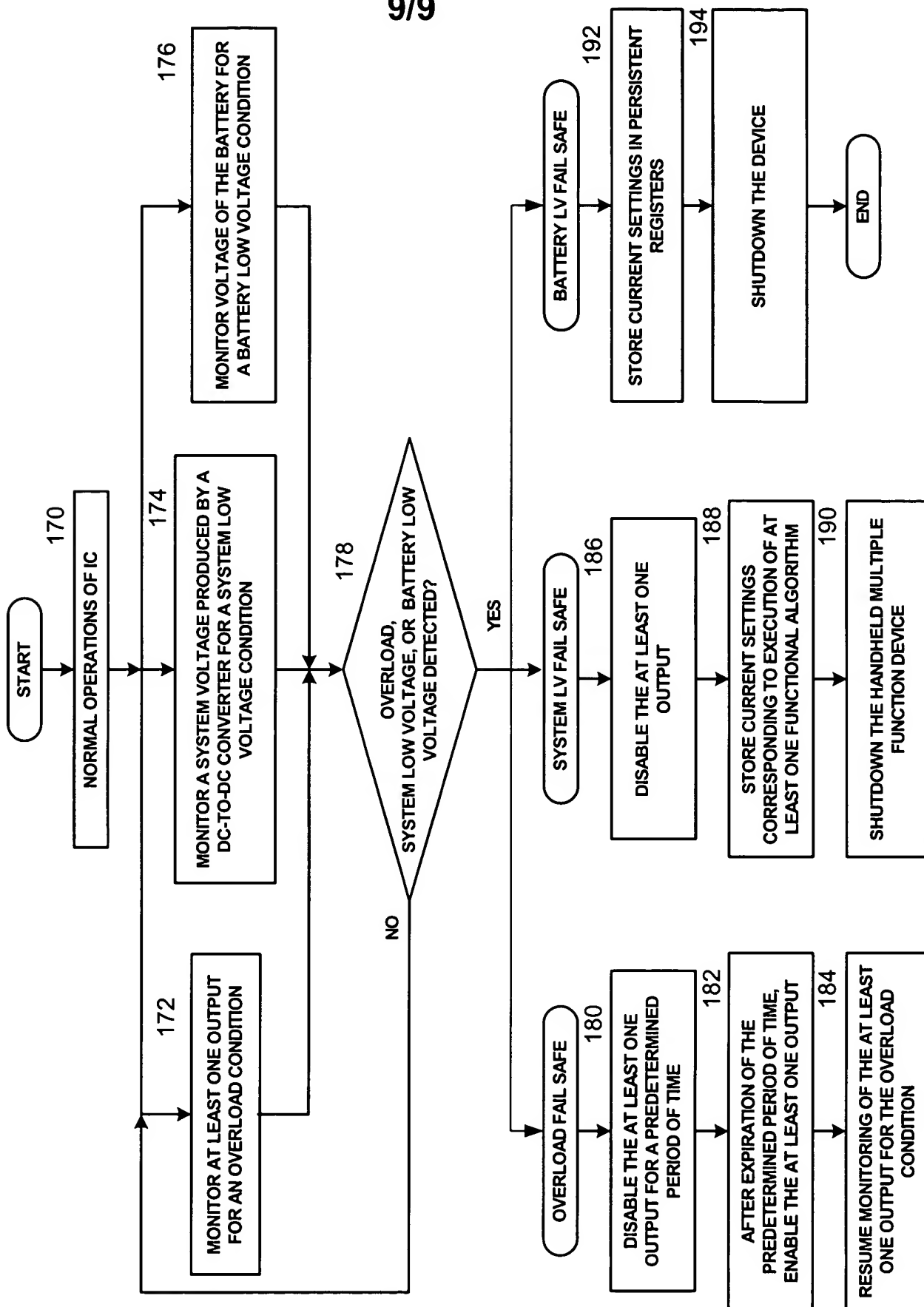


FIG. 10